

FIG. 1

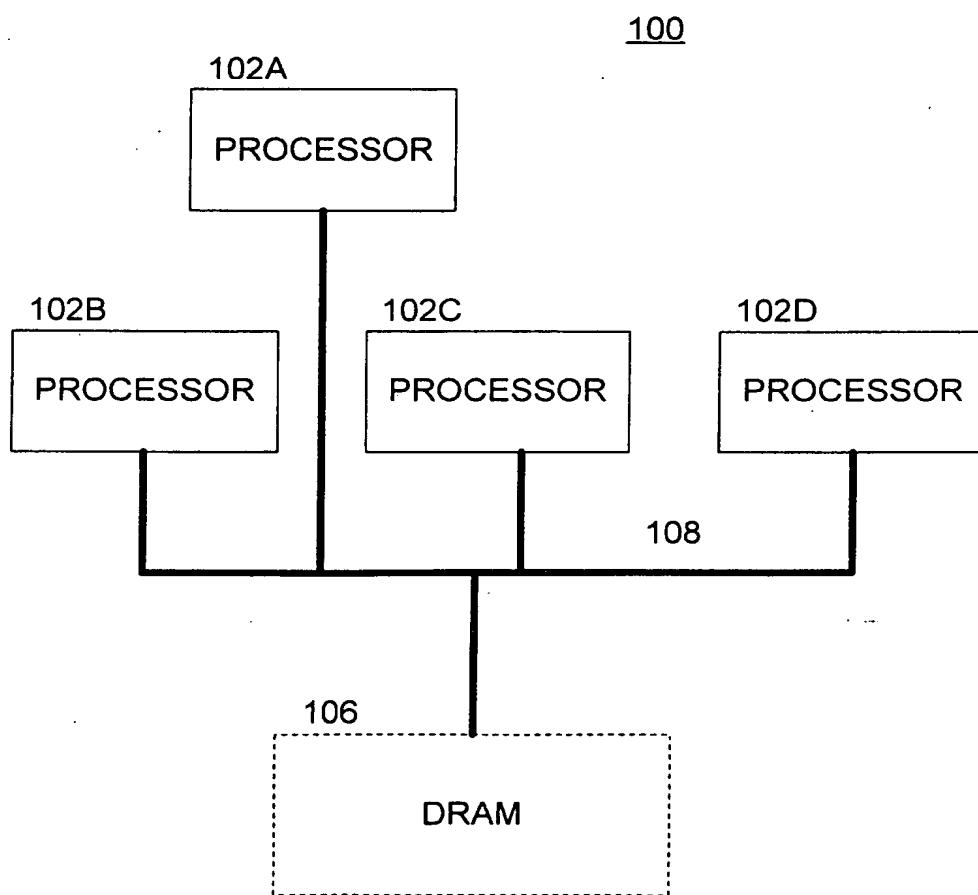


FIG. 2

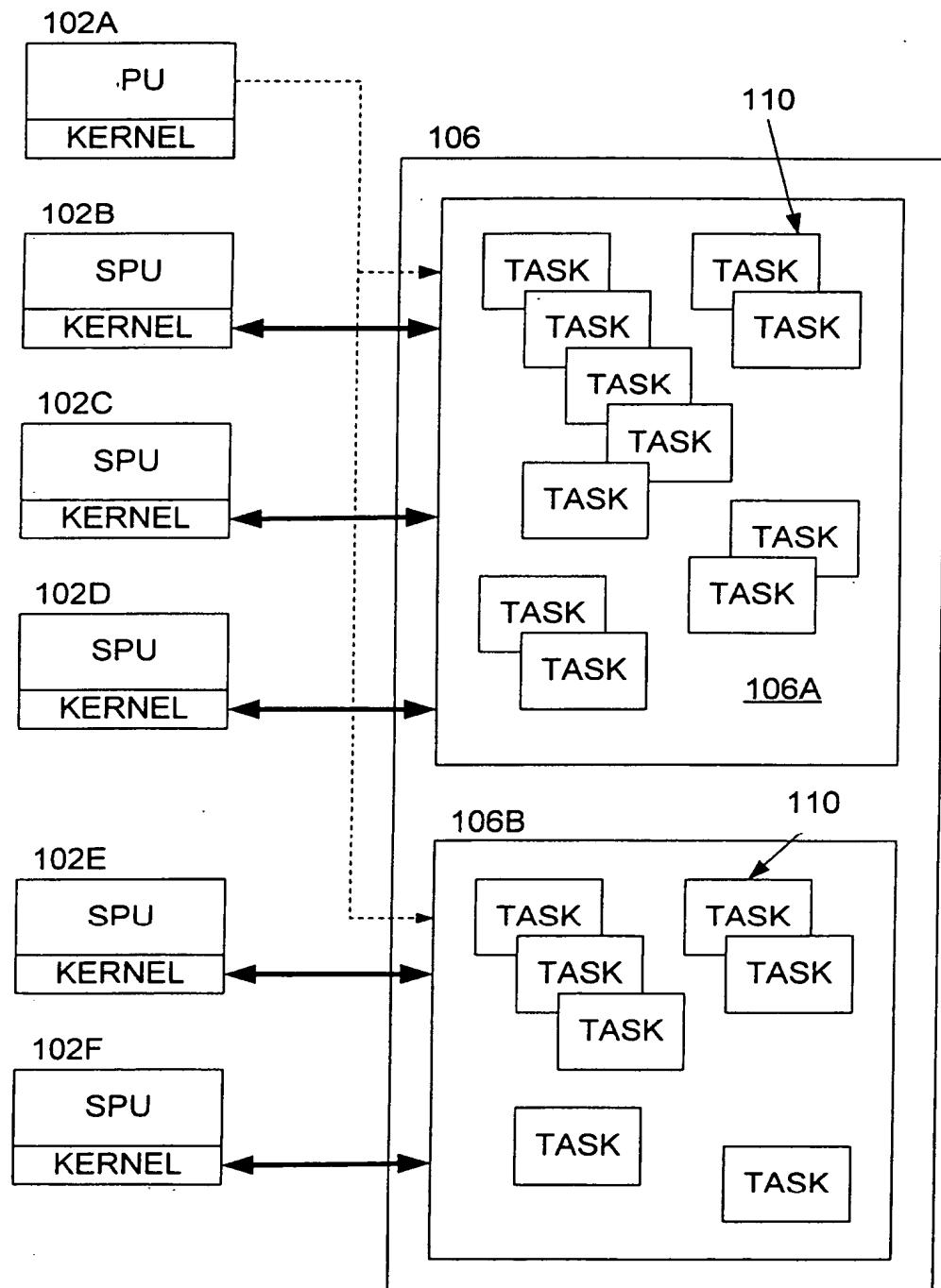


FIG. 3

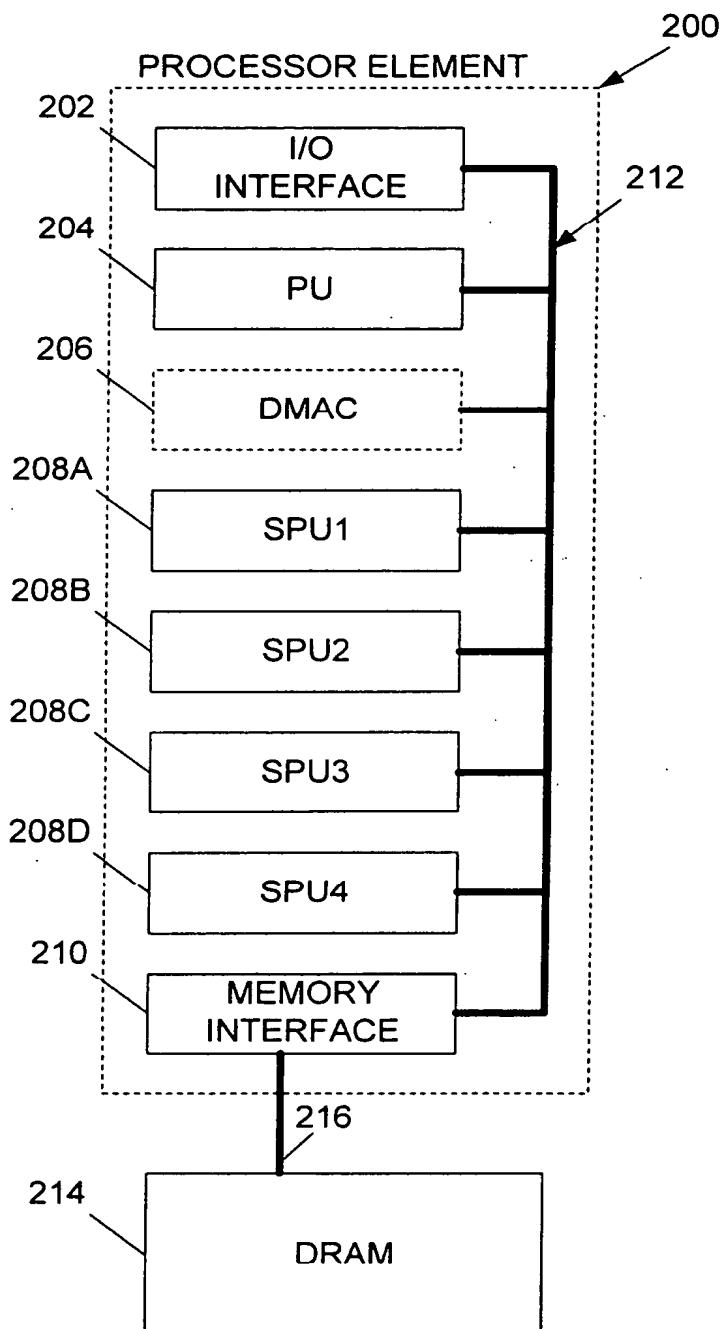
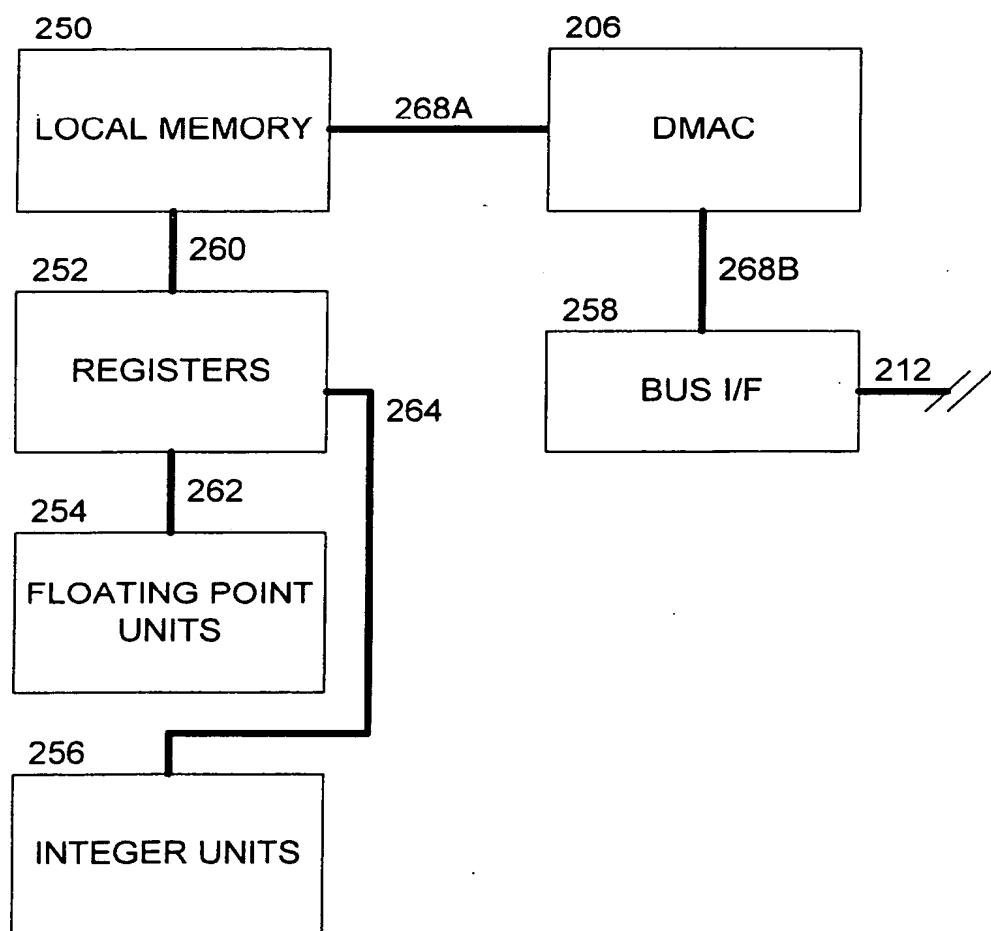


FIG. 4

208

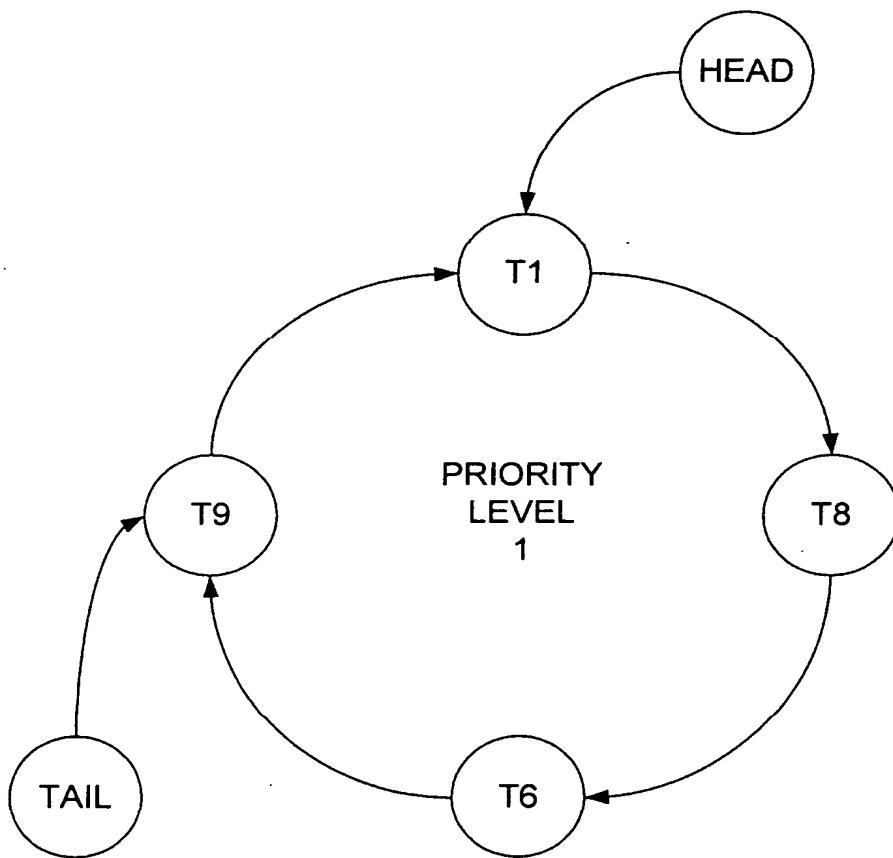
# FIG. 5

TASK TABLE

280

T1		T2		T3		T4	
PREV T9	NEXT T8	PREV T4	NEXT T10	PREV T5	NEXT T7	PREV T10	NEXT T2
STAT ready	PRI 1	STAT ready	PRI 2	STAT ready	PRI 3	STAT ready	PRI 2
T5		T6		T7		T8	
PREV T7	NEXT T3	PREV T8	NEXT T9	PREV T3	NEXT T5	PREV T1	NEXT T6
STAT ready	PRI 3	STAT ready	PRI 1	STAT ready	PRI 3	STAT ready	PRI 1
T9		T10					
PREV T6	NEXT T1	PREV T2	NEXT T4				
STAT ready	PRI 1	STAT ready	PRI 2				

FIG. 6



## FIG. 7

TASK QUEUE      282

PRIORITY	HEAD	TAIL
1	T1	T9
2	T10	T2
3	T7	T3
4	--	--
5	--	--
6	--	--
7	--	--
8	--	--
.		
.		
.		
n	--	--

FIG. 8

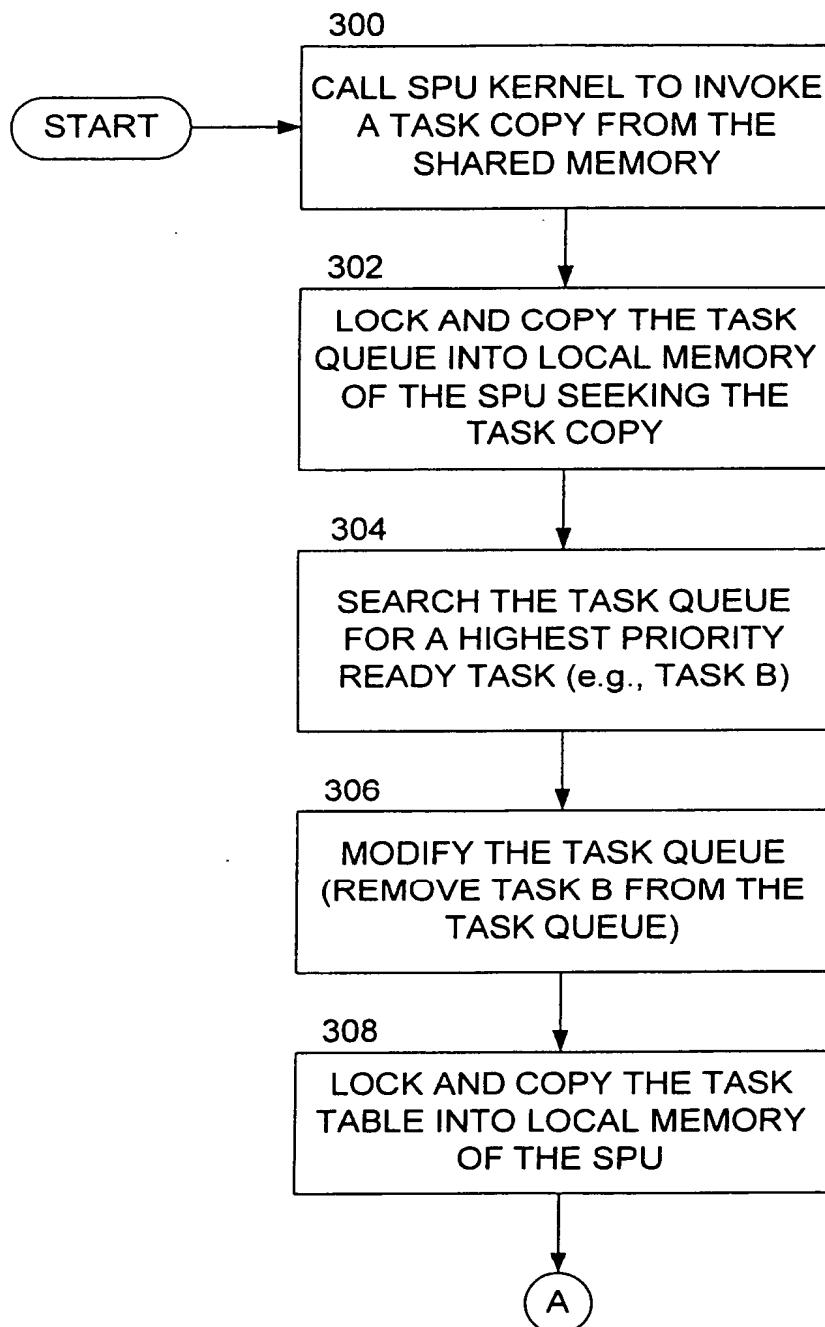
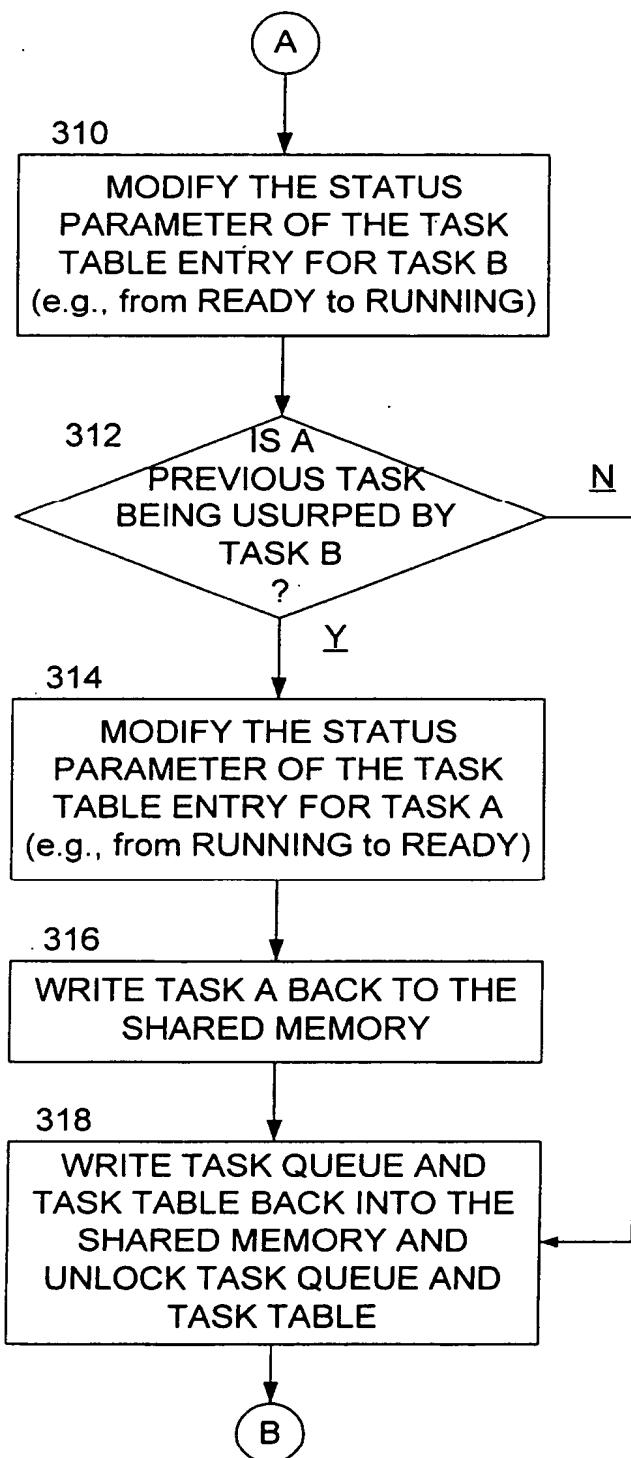


FIG. 9



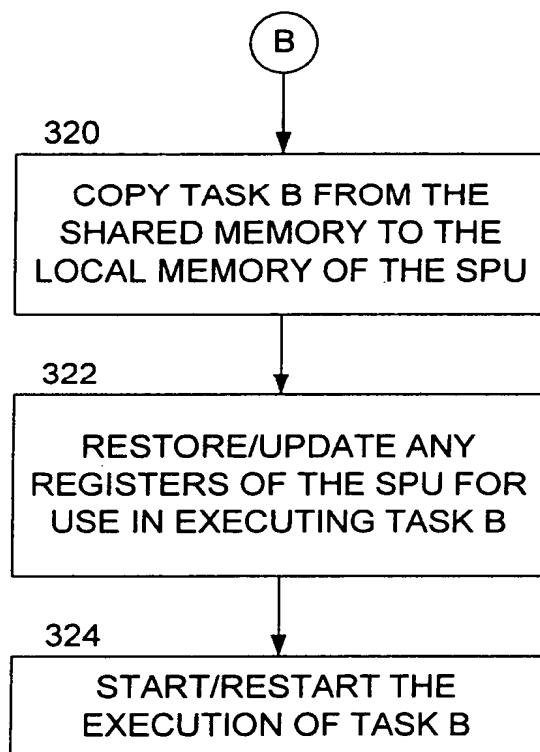
**FIG. 10**

FIG. 11

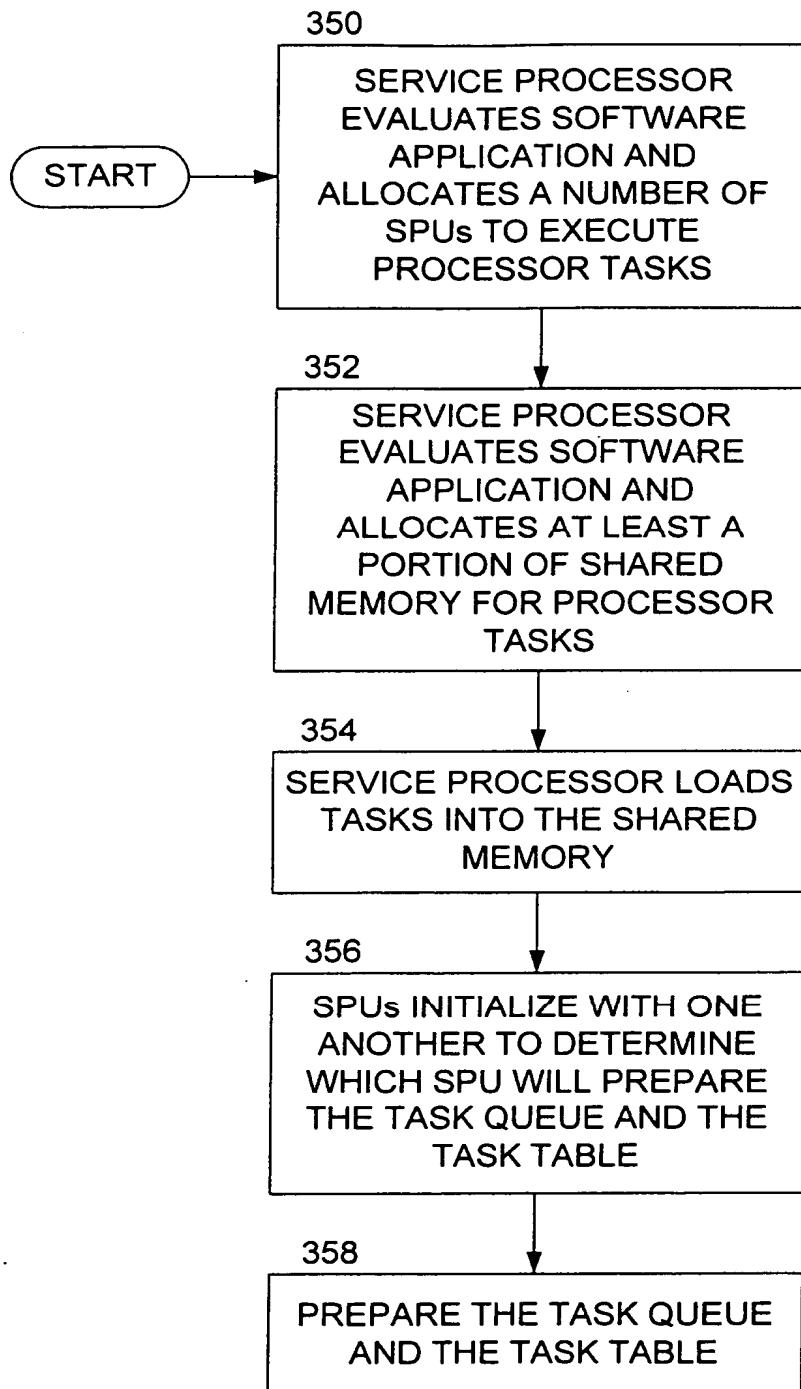


FIG. 12

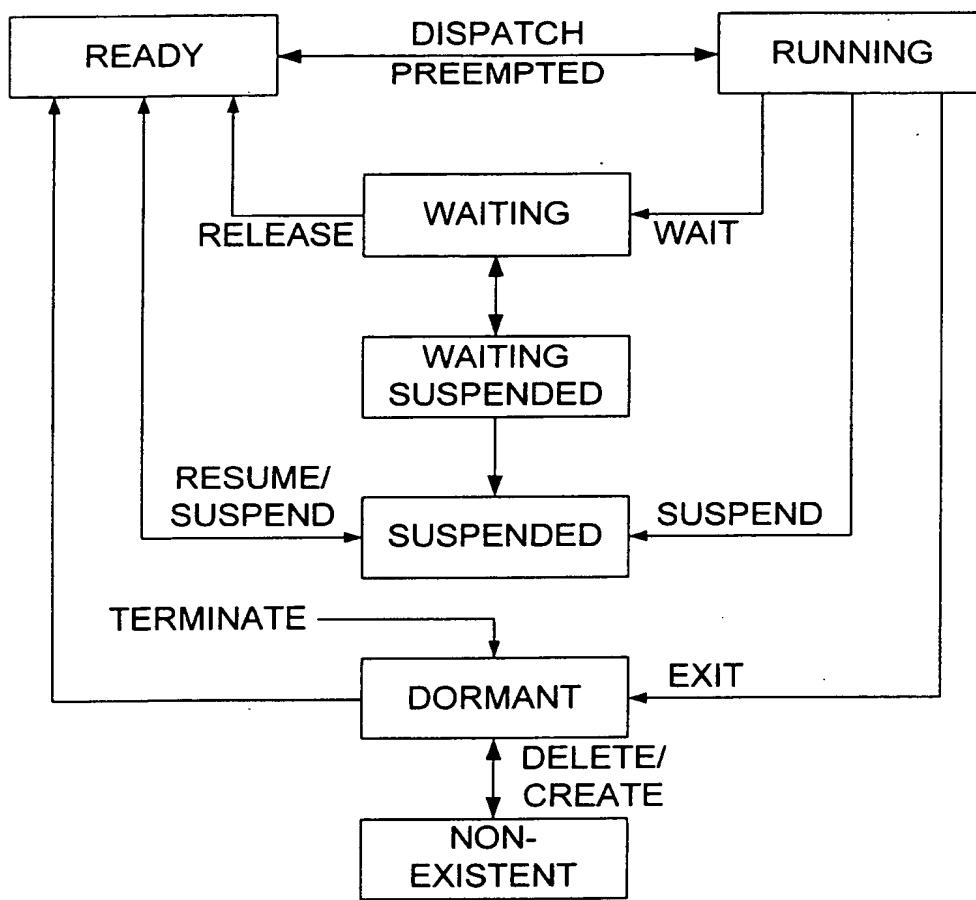


FIG. 13

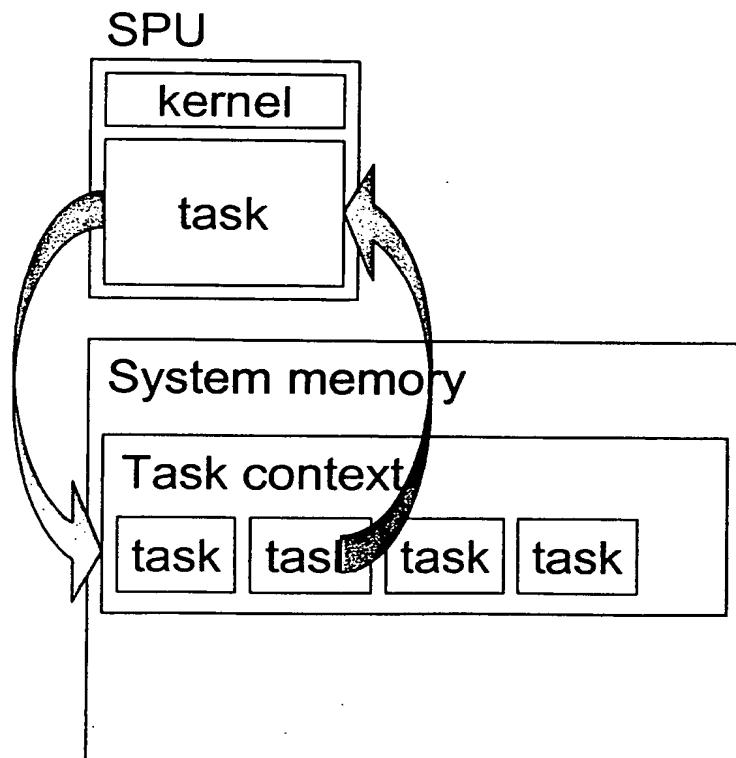


FIG. 14

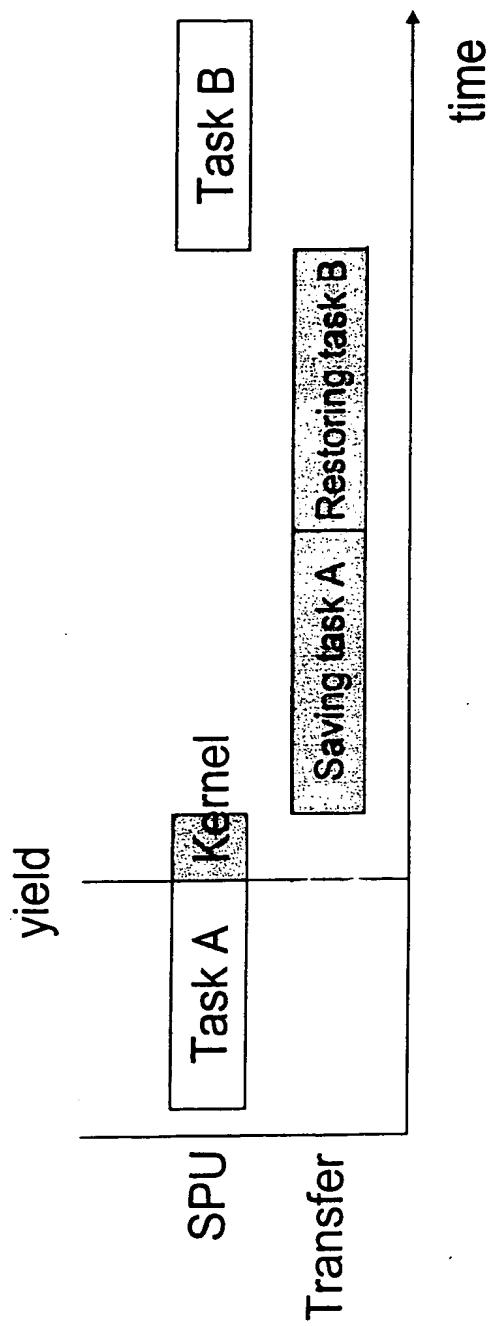


FIG. 15

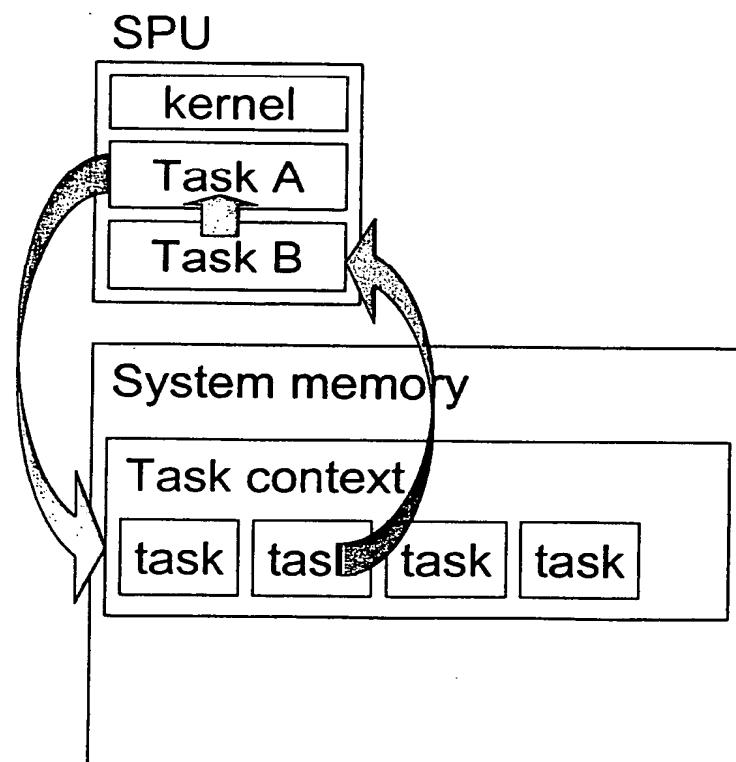


FIG. 16

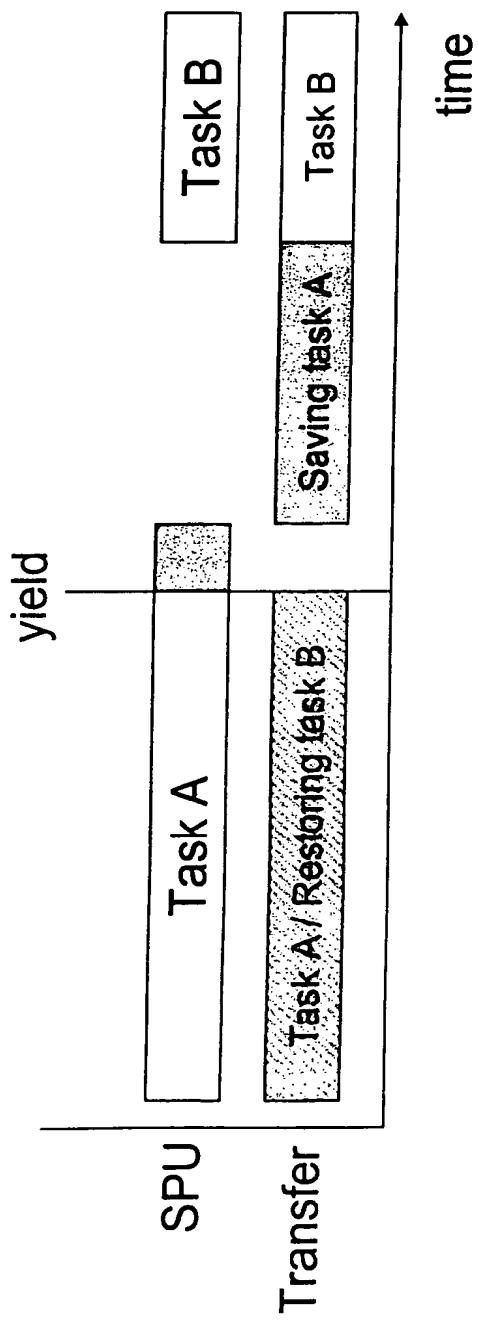


FIG. 17

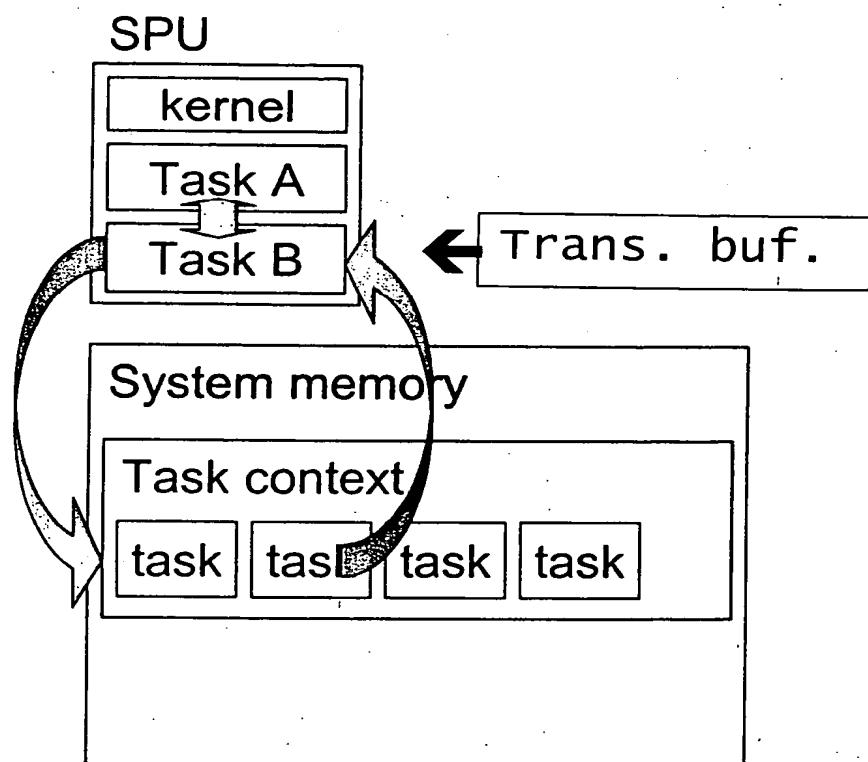


FIG. 18

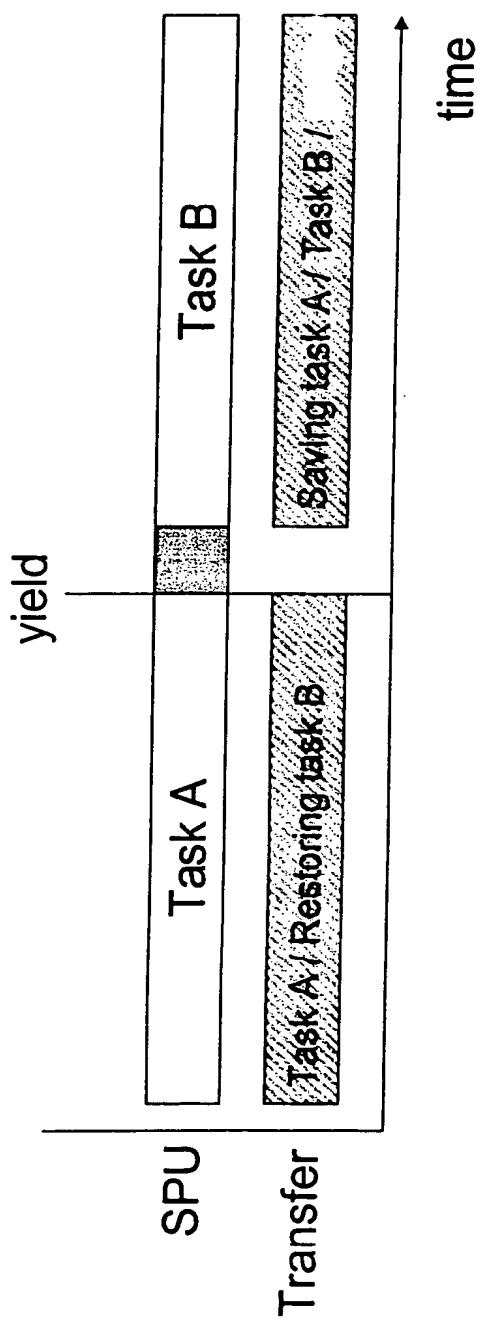
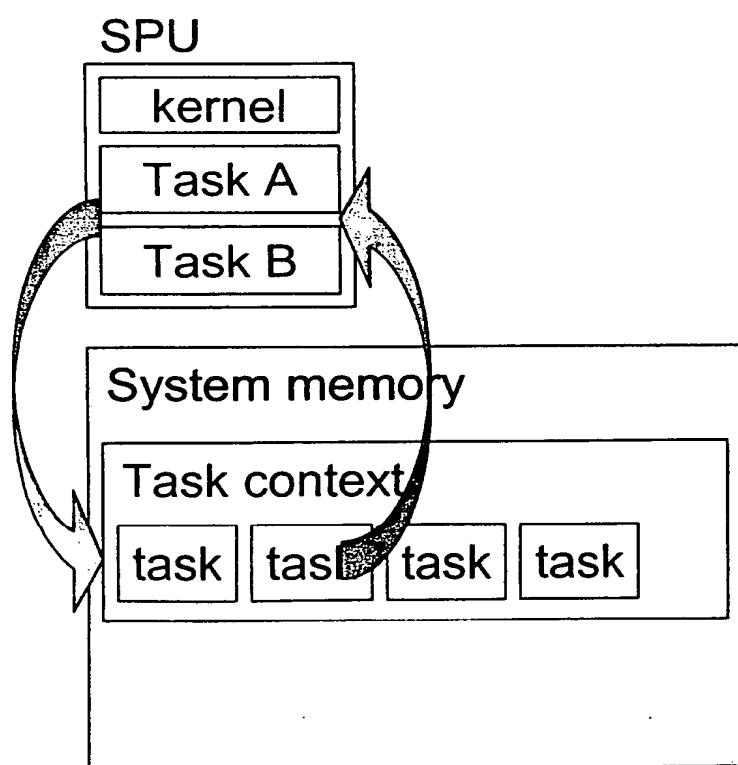


FIG. 19



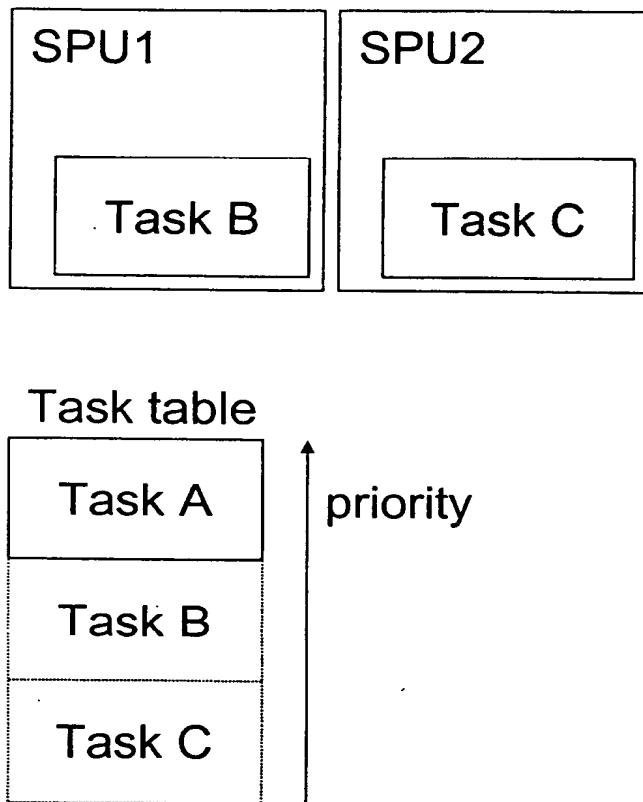
**FIG. 20**

FIG. 21

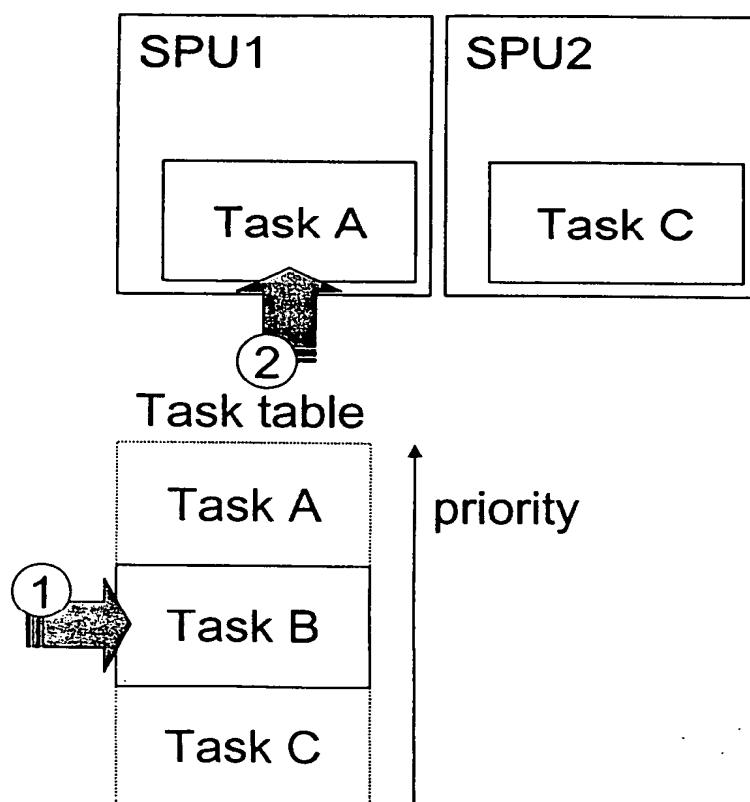


FIG. 22

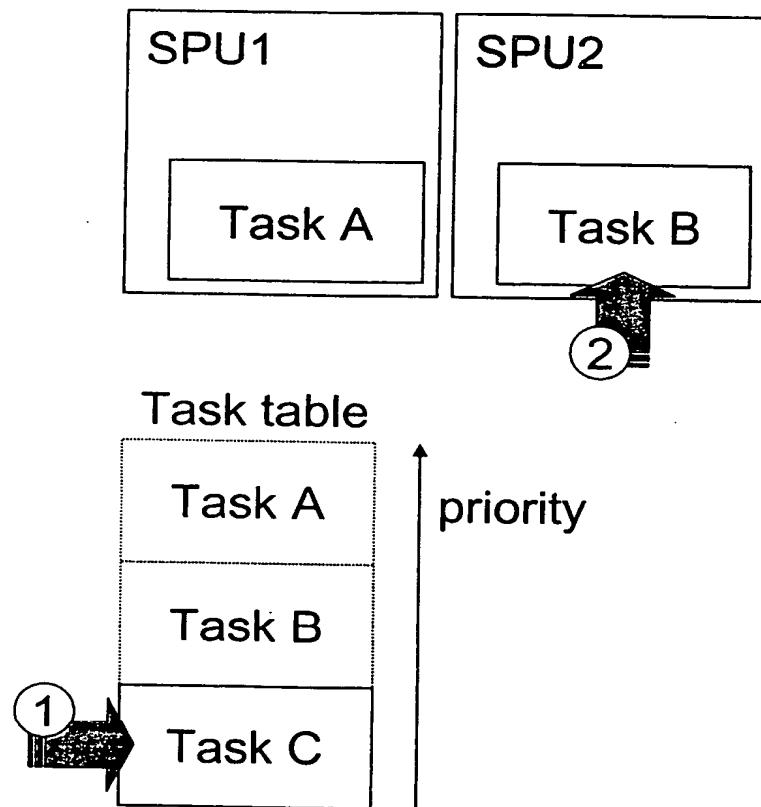


FIG. 23

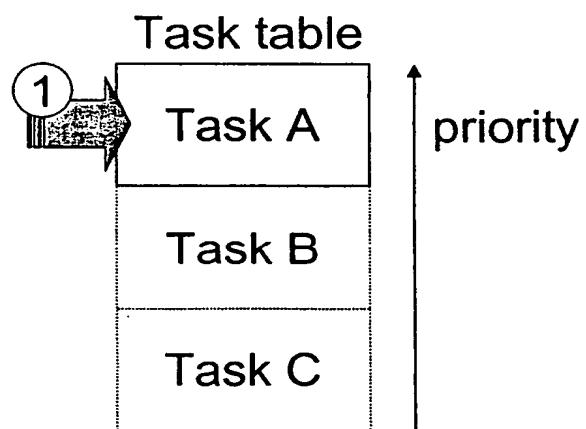
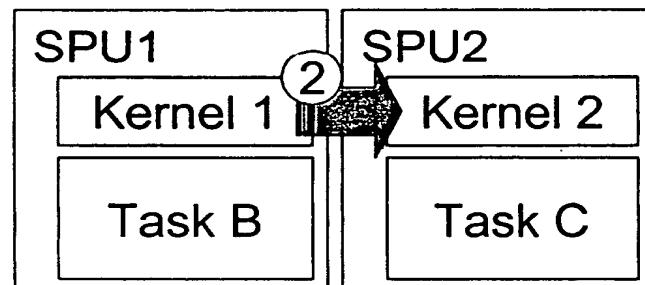


FIG. 24

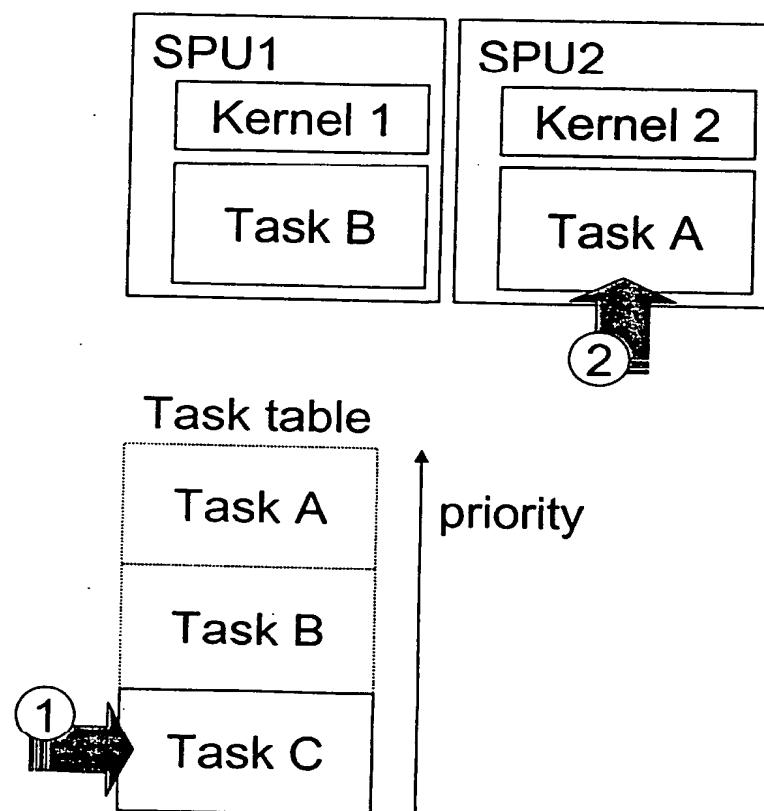


FIG. 25

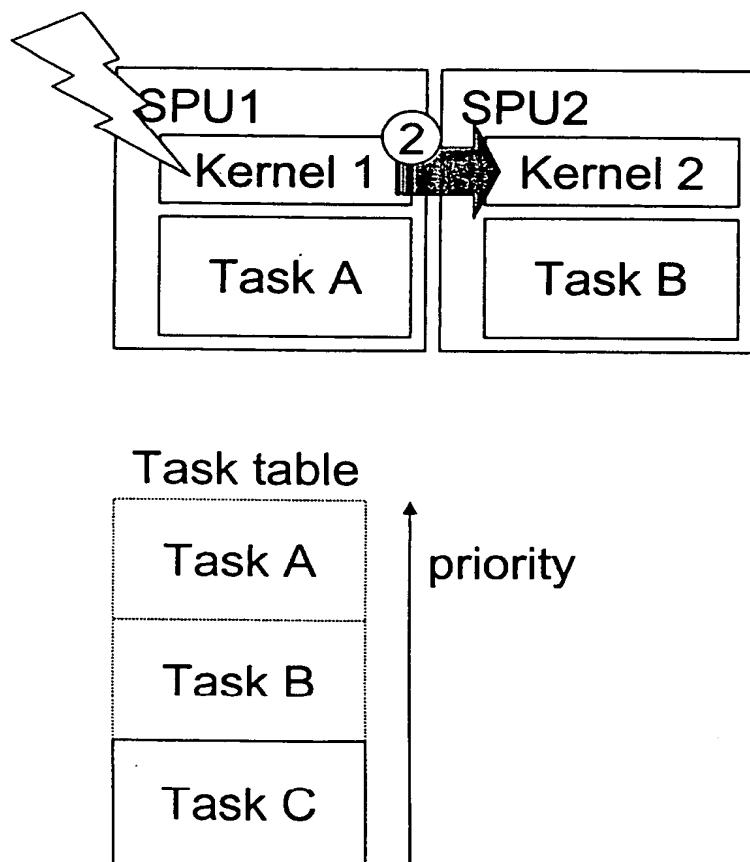


FIG. 26

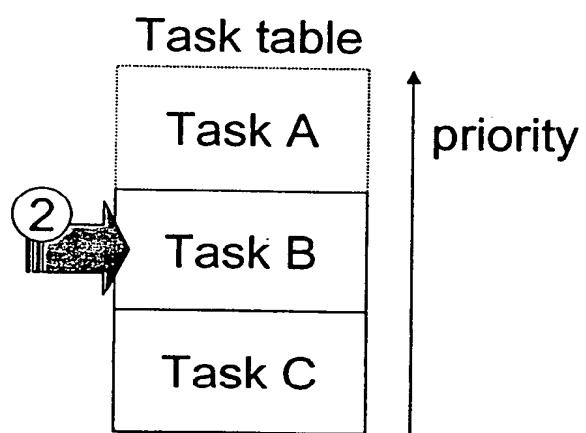
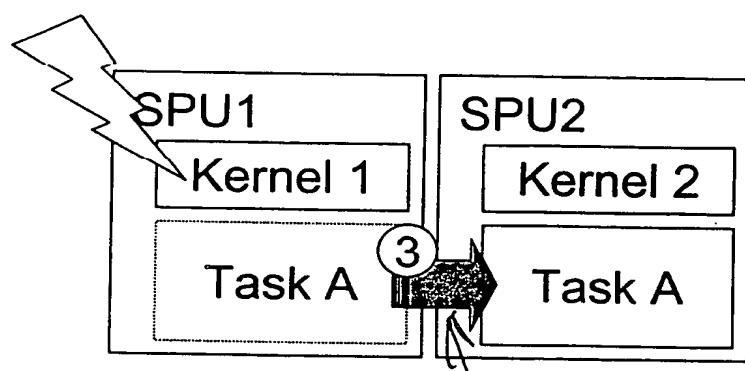


FIG. 27

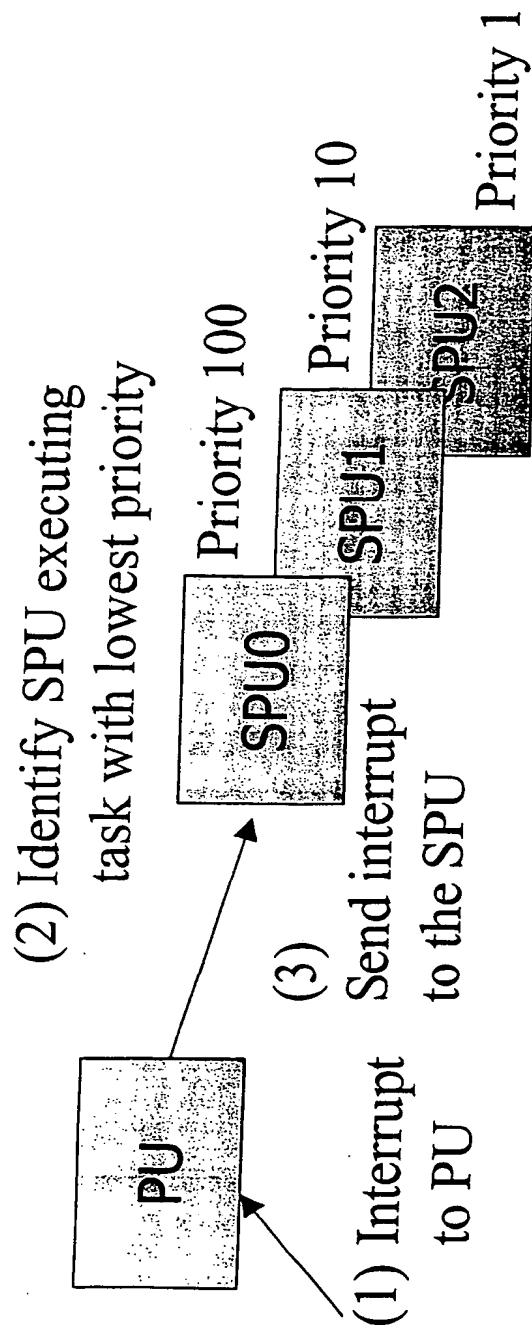


FIG. 28

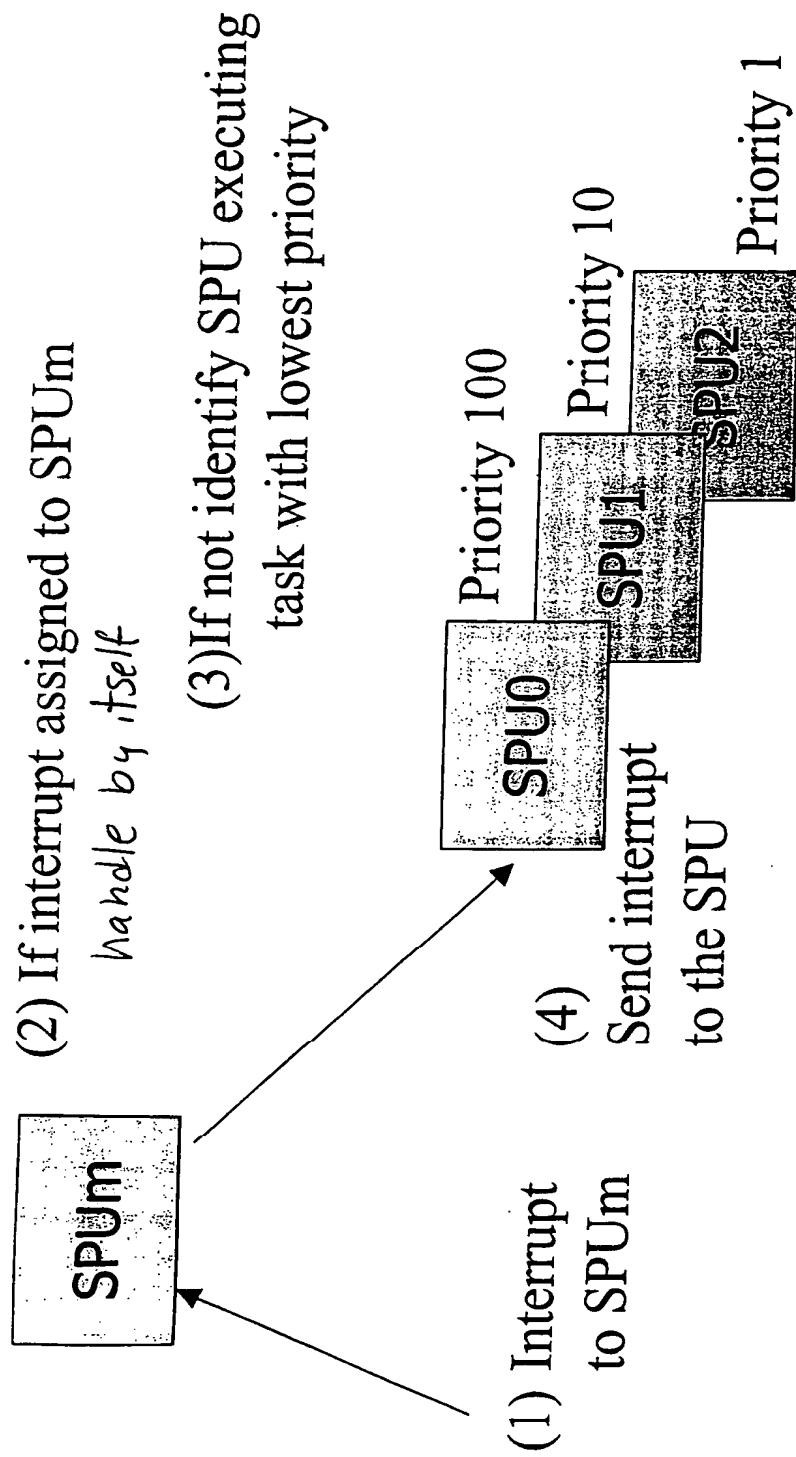


FIG. 29

Interrupt A      Interrupt B, C      Interrupt D, E, F

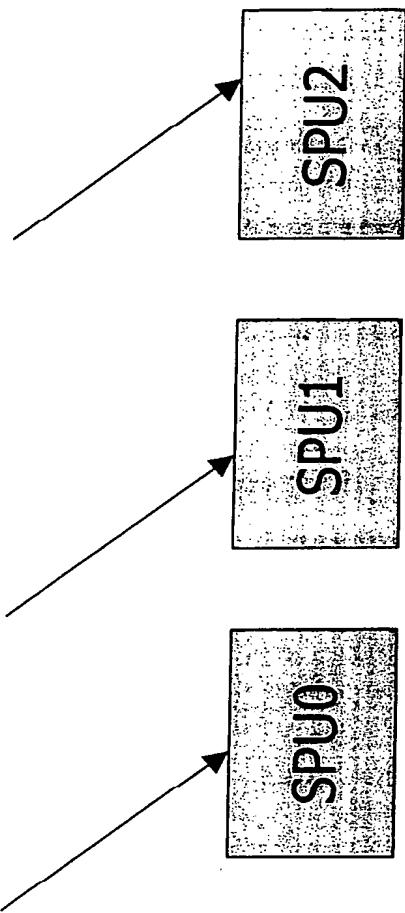


FIG. 30

290

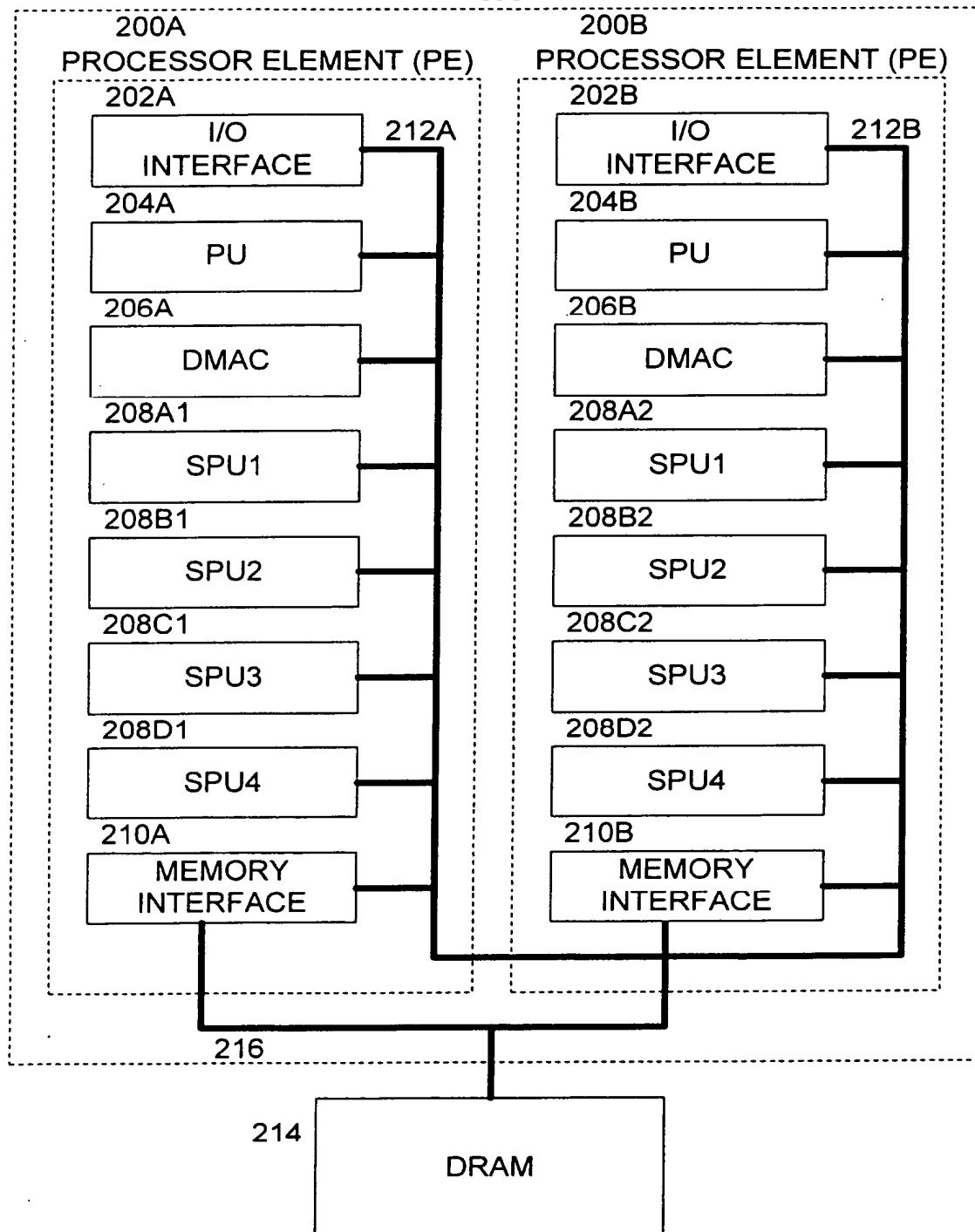


FIG. 31

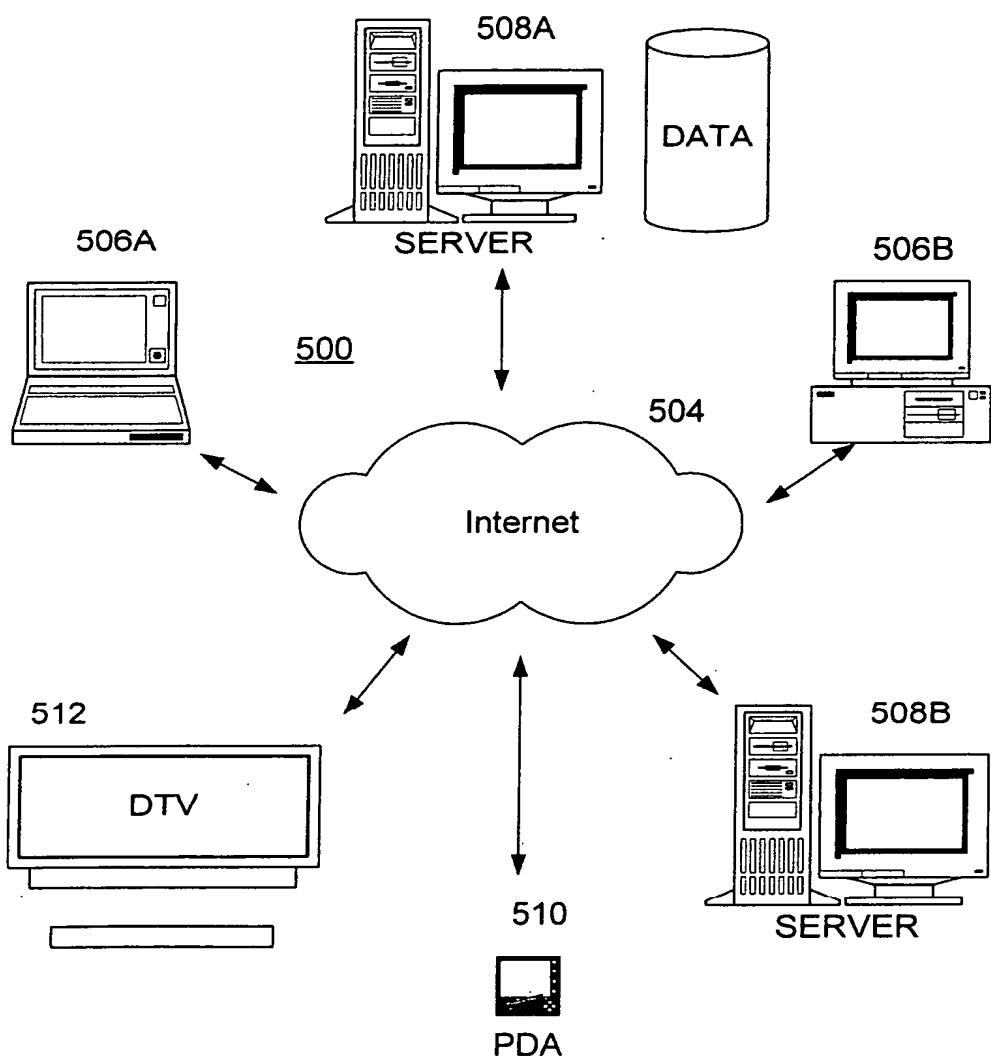


FIG. 32

